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L5	65	(microcontroller and (lock$step synchro$9) and clock).clm.	US-PGPUB	OR	OFF	2006/08/08 16:29
L6	5	5 and (emulat$5).clm.	US-PGPUB	OR	OFF	2006/08/08 16:27
L7	0	(microcontroller and emulat$5 and gatekeeper).clm.	US-PGPUB	OR	ON	2006/08/08 16:29
L8	6	(microcontroller and emulat$5 and virtual).clm.	US-PGPUB	OR	ON	2006/08/08 16:30
L9	2	(microcontroller and emulat$5 and timer).clm.	US-PGPUB	OR	ON	2006/08/08 16:30
L10	1	(microcontroller and emulat$5 and (break$point halt)).clm.	US-PGPUB	OR	ON	2006/08/08 16:30

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L11	1744	(microcontroller and (virtual (gate adj array)) and emulat$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/08 16:32
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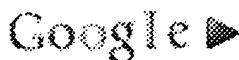File Format: PDF/Adobe Acrobat
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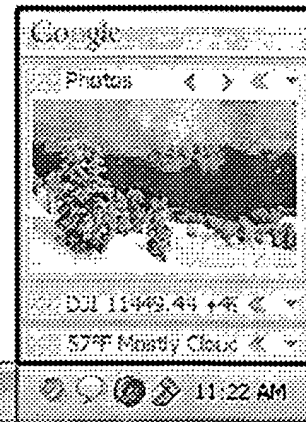File Format: Unrecognized
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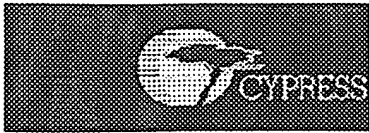
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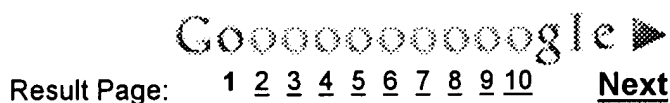Cached - Similar pages

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